

Introduction to Digital System (EE2016C)

Time / Location: Tuesday 13:00-14:50 (E1-121), Thursday 13:00-13:50 (E1-121)

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Office Hours: Thursday 14:00-15:00; other time by appointment only.

Teaching Assistant:

- **Tsung-Chih Tsai** (蔡宗智), office: E1-012 (ext:4586), Email: kager@seed.net.tw
- **Chin-Cheng Kuo** (郭晉誠), office: E1-012 (ext:4586), Email: 92521014@cc.ncu.edu.tw

Prerequisites: none

Text Book: M. Morris Mano, "Digital Design", 3rd Edition, Prentice Hall Inc., 2002.
ISBN 0-13-062121-8.

Reference: 1. Victor P. Nelson, etc., "Digital Logic Circuit Analysis and Design", Prentice Hall Inc., 1995.
2. C. H. Roth, Jr., "Fundamentals of Logic Design", 4th Ed., PWS Publishing Co., 1995.
ISBN 053495472-3.

Course Contents:

- Binary Systems
- Boolean Algebra and Logic Gates
- Gate-Level Minimization
- Combinational Logic
- Synchronous Sequential Logic
- Registers and Counters
- Memory and Programmable Logic

Grading:

Homework: 30% (assigned each week)
Midterm: 30%
Final Exam: 40%

On-Line Resources: Lecture notes, homeworks / tests, sample solutions, grading information, and other course-related materials are available at <http://www.ee.ncu.edu.tw/~jimmy/courses/DCS04>

Academic Honesty: Cheating is very uncivilized behavior and is to be avoided at all cost. Oral discussion about homeworks is not considered cheating. Copying someone else's homeworks / tests or part of a homework / test is cheating. If cheating is discovered, all students involved will receive no credit for the homework / test and possibly get an F grade for the course.