

CAD for VLSI Design

Homework #3

(Due: Apr. 30)

1. floorplanning:

- Given a normalized Polish expression $E = 21H54H7V36VHV$, please draw its corresponding slicing tree and floorplan respectively.
- Using the floorplan given in (a) as initial configuration, please give an example for the perturbation M1 and M2 in the simulated annealing approach respectively on the floorplan.

- Please apply cluster growth algorithm and show the step-by-step solutions to determine the placement of the following circuit. (Please place it as a 4x4 matrix.)

