

## Digital System Design

### Homework #6

(Due: May 4)

1. Write an HDL behavioral description of a 4-bit comparator with a 6-bit output  $Y[5:0]$ . Bit 5 of  $Y$  is for equal, bit 4 for unequal, bit 3 for greater than, bit 2 for less than, bit 1 for greater than or equal, and bit 0 for less than or equal to.
2. 撰寫適當的 test bench, 觀察前一題的結果.

#### 作業繳交方式:

繳交電子檔即可, 繳交管道及評分方式請見助教於 BBS 上的公告