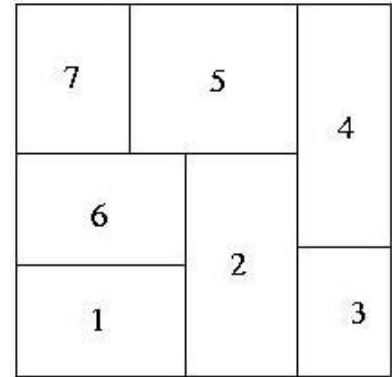


Introduction to Electronic Design Automation (EE4026)

Homework #2 (Due: Apr. 30)

1. floorplanning:

- (a) Given a floorplan shown right, please draw its corresponding skewed slicing tree.
- (b) Please also draw the corresponding polar graphs of the right floorplan.
- (c) Given a normalized Polish expression $E = 21H54H7V36VHV$, please draw its corresponding slicing tree and floorplan respectively.



2. deterministic placement:

- (a) For the gate-level circuit shown right, please use cluster growth algorithm to find a single-row placement for those gates.
- (b) Assume the distance between two adjacent gates is 1, please estimate the total wire length and longest wire length of your placement in (a).

