Introduction to Electronic Design Automation (EE4026)

Homework #5 (Due: Jun. 11)

1. Please use DAGON algorithm to find the minimum-cost covering of the subject graph by using the given cells. Please redraw the subject graph on your answer sheet and show the final covering with the best results of all nodes.



- 2. For the circuit shown below:
 - (a) Assume the delay time of all gates is equal to 1. Please show the longest and shortest paths individually along with their delay values. Please also briefly explain your method to calculate the delay values.
 - (b) Assume the delay time of inverter is 1, the delay time of XOR gate is 3, and the delay time for other gates is 2. Please show the longest and shortest paths individually along with their delay values. Please also briefly explain your method to calculate the delay values.

